








Power source voltage regulator device incorporated in LSI circuit.

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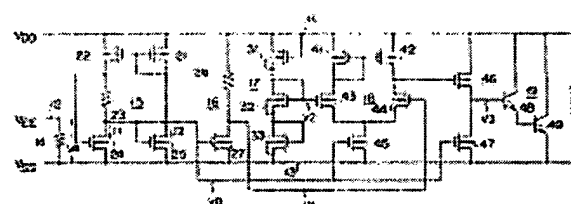
Cited documents:

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 EP0021289
 GB2081458
 US4306183
 US4454467

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Abstract of EP0239989

A voltage regulator for an output voltage of a solar cell is formed together with an LSI circuit on a single chip. The voltage regulator includes a bias circuit (15) as a CMOS current mirror circuit constituted by MOS transistors designed to operate in weak inversion regions, a constant current circuit constituted by a parasitic bipolar transistor, a voltage divider (17) having a plurality of MOS transistors whose current paths are connected in series with each other, a comparator (18) constituted by a CMOS differential amplifier, and a current path of a CMOS transistor, thereby assuring low current consumption, a highly stable regulated output, and a high packing density of the LSI circuit.

FIG. 6

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